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22717 ELCOM B
32547 AMI A

Dawe! chip

21 NOVEMBER 1983

TO. MR. PETER SQUIRELL
FROM: MR. HERBERT GRUENBACHER, AMI (A)

FINAL PIN CONNECTIONS

PRELIM. PIN NO.	FINAL PIN NO.	TITLE	TYPE
(1)		RH0	O/D
(2)		RH1	O/D
(3)		RH2	O/D
(4)		RH3	O/D
(5)		RH4	O/D
(6)		RH5	O/D
(7)		LH0	O/D
(8)		LH1	O/D
(9)		LH2	O/D
(10)		LH3	O/D
(11)		LH4	O/D
(12)		LH5	O/D
(13)		/WR0	0
(14)		/WR1	0
(15)		/WR2	0
(16)		D0	I/O
(17)		D1	I/O
(18)		D2	I/O
(19)		D3	I/O
(20)		/VIO	0
(21)	65	/M1	I
(22)		/RSTI	S
(23)		/CAP	S
(24)		A0	I
(25)		A1	I
(26)		VDD	
(27)		A2	I
(28)		A3	I
(29)		A4	I
(30)		A5	I
(31)		A6	I
(32)		A7	I
(33)		D4	I/O
(34)		D5	I/O
(35)	36	D6	I/O
(36)	37	D7	I/O
(37)	38	/WR	I
(38)	39	/IORQ	I
(39)	35	/RD	I
(40)		/RSTO	0
(41)		/INT1	S
(42)		/INT2	S
(43)		/IRQ	O/D
(44)		A14'	I
(45)		A15'	I
(46)		/BREQ	I
(47)		A14	T/S
(48)		A15	T/S
(49)		A16	T/S
(50)		A17	T/S
(51)		A18	T/S
(52)		A19	T/S

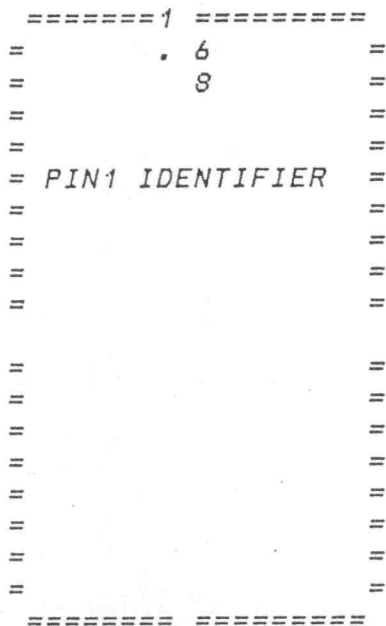
(48)		A15	T/S
(49)		A16	T/S
(50)		A17	T/S
(51)		A18	T/S
(52)		A19	T/S
(53)		A20	T/S
(54)		A21	T/S
(55)		/VRAM	0
(56)		/ROM	0
(57)		/CART	0
(58)		/MRES	I
(59)		/TEST	I
(60)		VSS	
(61)		/MREQ	I
(62)		CLK	I
(63)		ZCK	I
(64)		/WAIT	O/D
(65)	21	1MHZ	0
(66)		/RDO	0
(67)		/RD1	0
(68)		/RD2	0

I = TTL INPUT
0 = TTL OUTPUT
I/O = TTL BI-DIRECTIONAL
O/D = OPEN DRAIN OUTPUT
S = SCHMITT TRIGGER INPUT
TS = TRI-STATE OUTPUT
(/ = ACTIVE LOW

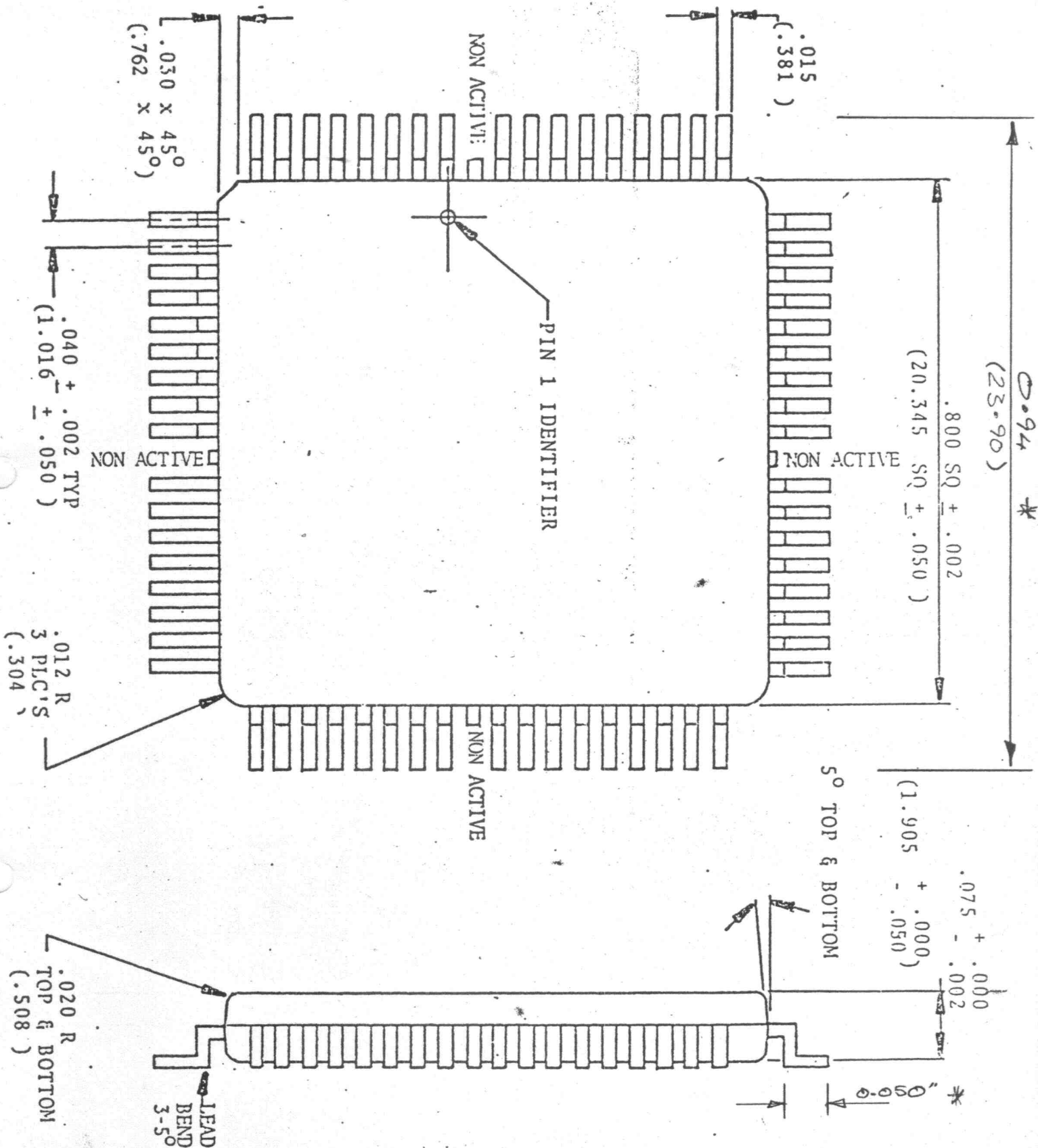
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68 LEAD PLASTIC MINI-FLAT PACKAGE

FXXX TOP VIEW



BEST REGARDS,
HERBERT GRUENBACHER, AMI AUSTRIA



* = DIMENSION NOT ON ORIGINAL DRAWING - OBTAINED FROM MEASUREMENT OF DEVICE.

68 LEAD QUAD
PACKAGE OUTLINE

AMI AMERICAN MICROSYSTEMS, INC.
3900 Homestead Rd. Santa Clara, CA 95051

Drawing Number
6000055
Code Ident.

PAD SIZE ON PCB IS 0.070" x 0.020"